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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gordon E. Morrison

Group Art Unit: 237

Prior Serial No. 560,093

Prior Examiner: E. Chan

Parent Filed: July 30, 1990

Title: "Parallel Processing Method and Apparatus
for Increasing Processing Throughput by
Parallel Processing Low Level Instructions
Having Natural Concurrencies"

Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

PRELIMINARY AMENDMENT

In response to the Office Action mailed January 14, 1992 in
the parent case, please amend the application as follows.

In the Claims

Cancel claim 79.

Amend claims 71, 72, 74-78, and 80-83 as follows:

71. (Twice Amended) A system for executing branches in
single entry-single exit (SESE) basic blocks (BBs) contained
within a program, each basic block having a plurality of non-
branch instructions and ending with a branch instruction, said
system comprising:

means receptive of said program for determining the
branch instruction within each said basic block of said program,
said determining means further adding firing time information to
said branch instruction,